

CRYSTAL OSCILLATOR (SPXO)

OUTPUT: HCSL





Product Number SG2016HGN: X1G006221xxxx15 SG2520HGN: X1G005891xxxx15

SG2016HGN / SG2520HGN

25 MHz to 500 MHz •Frequency range Supply voltage 2.5 V Typ. / 3.3 V Typ. Frequency tolerance $\pm 25 \times 10^{-6}, \pm 50 \times 10^{-6}$

•Operating temperature range -40 °C to +85 °C, -40 °C to +105 °C Function Output enable (OE) or Standby (ST)

Phase jitter 90 fs Max.

 $(100 \text{ MHz} < \text{fo} \le 156 \text{ MHz}, V_{CC} = 2.5 \text{ V}, 3.3 \text{ V})$





 $(2.0 \times 1.6 \times 0.63 \text{ mm})$ $(2.5 \times 2.0 \times 0.74 \text{ mm})$

•PCIe Gen5,6 Jitter specification compliant.

Specifications (ch	aracteristi	(5)		
Item	Symbol	Specifications	Conditions / Remarks	
Output frequency range	fo	25 MHz to 500 MHz	Please contact us for available frequencies.	
Supply voltage	V _{cc}	D: 2.5 V ± 5 %, C: 3.3 V ± 5 %		
Storage temperature range	T_stg	-55 °C to +125 °C		
Operating temperature range	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C		
Frequency tolerance	f_tol	D: ±25 × 10 ⁻⁶ Max. J: ±50 × 10 ⁻⁶ Max.	Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)	
Current consumption	I _{cc}	35 mA Max. 40 mA Max.	25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 500 MHz	OE or $\overline{ST} = V_{CC}$, L HCSL = 50 Ω
Disable current	I dis	25 mA Max.	OE = GND	· -
Stand-by current	_	30 μA Max.	ST = GND, T use Max. = +85 °C	
	I_std -	60 μA Max.	ST = GND, T use Max. = +	
Symmetry	SYM	45 % to 55 %	At output crossing point	
Output voltage	Voн	0.5 V to 0.7 V	25 MHz ≤ fo < 212 MHz	Output option: A
		0.4 V to 0.65 V	212 MHz ≤ fo < 500 MHz	
		0.6 V to 0.8 V	25 MHz ≤ fo < 212 MHz	Output option: B
		0.5 V to 0.75 V	212 MHz ≤ fo < 500 MHz	
	Vol	-0.15 V to +0.15 V		1
Differential swing	Vsw	0.7 V to 1.4 V	Output option: A	
		0.8 V to 1.6 V	Output option: B	
Crossing voltage	V _{CR}	0.25 V to 0.55 V		
Rise time / Fall time	tr/tf	0.7 ns Max.	20 % - 80 % (V _{OH} - V _{OL})	
Differential output rise slew rate / fall slew rate	Rr/Rf	2 V/ns to 10 V/ns	Between -0.15 V and 0.15 V of differential output	
Output load condition	L_HCSL	50 Ω		
Input voltage	V _{IH}	70 % V _{CC} Min.	OE or ST terminal	
	VIL	30 % V _{CC} Max.		
Output enable time	tsta_oe	500 ns Max.	t = 0 at OE = 70 % V _{CC}	
	tsta_st	10 ms Max.	t = 0 at ST = 70 % V _{CC}	
Output disable time	tstp_oe	100 ns Max.	t = 0 at OE = 30 % V _{CC}	
	tstp_st	100 ns Max.	t = 0 at ST = 30 % V _{CC}	
Start-up time	t_str	10 ms Max.	t = 0 at 90 % V _{CC}	
Phase jitter		200 fs Max.	25 MHz ≤ fo < 100 MHz	Offset frequency
	t _{PJ}	90 fs Max.	100 MHz ≤ fo ≤ 156 MHz	fo < 50 MHz:
		70 fs Max.	156 MHz < fo ≤ 212 MHz	12 kHz to 5 MHz
		60 fs Max.	212 MHz < fo ≤ 391 MHz	fo ≥ 50 MHz:
		50 fs Max.	391 MHz < fo ≤ 500 MHz	12 kHz to 20 MHz
Jitter	t _{c-c}	60 ps Max.	Cycle to cycle jitter (Peak to Peak)	
PCIe jitter limits		0.1 ps Max.	For PCIe Gen5 For PCIe Gen6	
for CĆ architecture	-	0.06 ps Max.		

Product name

Product Name (Standard form) <u>SG2016 HGN</u> <u>100.000000MHz</u> <u>C</u> <u>D</u> <u>H</u> <u>P</u> <u>Z</u> <u>A</u> defghi

a: Model b: Output (H: HCSL) c: Frequency d: Supply voltage e: Frequency tolerance

f: Operating temperature g: Function h: Output disable type (Z: High impedance) i: Output option

d: Supply voltage
C 3.3 V Typ.
D 2.5 V Typ.

e: Freq. tolerance D ±25 × 10⁻⁶ J ±50 × 10-6

f: Operating temp. G -40 °C to +85 °C H -40 °C to +105 °C

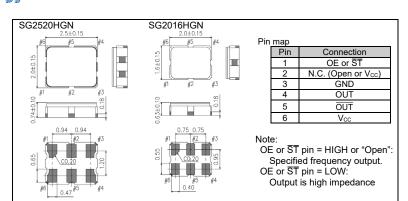
g: Function P OE S

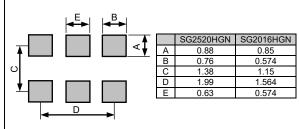
i: Output option
A V_{SW} = 0.7 V to 1.4 V B V_{SW} = 0.8 V to 1.6 V

External dimensions

Footprint (Recommended)

(Unit:mm)





In order to achieve optimum jitter performance, it is recommended that 0.1 μF and 10 μF bypass capacitors should be connected between $V_{\rm CC}$ and GND and placed as close to the $V_{\rm CC}$ pin as possible.

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major automotive manufacturers as standard.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



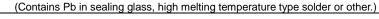
►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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