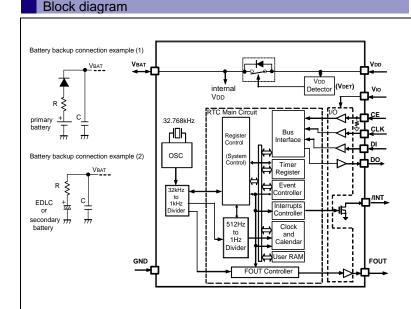
# Real time clock module

# REAL TIME CLOCK MODULE (SPI-Bus)

Time stamp function and Low current consumption

# **RX4111CE**

- Built in frequency adjusted 32.768 kHz crystal unit
- Operating Temperature : -40 °C to +105 °C
- Interface Type : SPI -Bus 4 wire
- Low backup current : 100 nA Typ. / 3 V
- · Auto power switching function: Automatically switches to backup power
- supply by monitoring the VDD voltage.
- Time stamp function : 8 times stamped from year to 1/256 seconds : Wake up every minute or every second
- Interrupt output
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption: Crystal oscillation stop, V<sub>BAT</sub> low, V<sub>DD</sub> low



# RoHS Compliant

Product Number (2,000 pcs / Reel) RX4111CE (A grade): X1B000431000115 RX4111CE (B grade): X1B000431000215



**RX4111CE** (3.2 x 2.5 mm, t = 1.0 mm Max.)

#### Overview

- Interface type
   SPI-Bus interface (4 wire, 1 MHz) Auto power switch function
  - The V<sub>DD</sub> voltage is monitored and it switches to the backup power supply by the automatic operation Backup power supply switching voltage 1.2V Min.
- Clock output function
- Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz Wakeup timer function
- Selectable from 244 µs to 32 years (24 bit 1 ch.) Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz Auto release after interrupt output from /INT pin at timer completes

This operation is auto repeat with a selected cycle, it can be used like a watchdog timer

Time stamp function

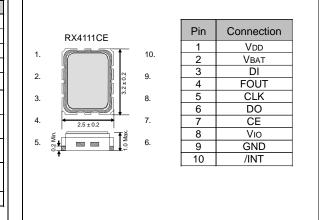
8 times stamped from year to 1/256 seconds The time stamp trigger inputs from self-monitoring and SPI command

- Alarm function
- It is possible program from year to second
- Self-monitoring interruption
- Crystal oscillation stop, VBAT low, VDD low

## Pin Functin

| Signal Name | 1/0                  | Function   |  |
|-------------|----------------------|--|--|
| CE          | Input                | Chip enables input pin   |  |
| CLK         | Input                | Serial clock input pin   |  |
| DI          | Input                | ata input pin  |  |
| DO          | Output               | Data output pin  |  |
| FOUT        | Output               | Frequency output (CMOS)<br>(frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)  |  |
| /INT        | Open-Drain<br>Output | Interrupts output by Alarm and Timer events. (N-ch. open drain)  |  |
| Vdd         | -                    | Power supply pin<br>Possible to supply different voltage from Vio  |  |
| Vio         | -                    | Interface power supply pin<br>Input to supply the voltage same as a host   |  |
| VBAT        | -                    | Power supply pin for backup battery<br>Connect an EDLC, a secondary battery, a primary battery<br>In the backup voltage range, supplied to IC, from this pin |  |
| GND         | -                    | Ground pin   |  |

#### Terminal connection / External dimensions (Unit: mm)



#### Specifications (characteristics)

| Recommended (             | Operat | ing Cond     | ditions                    |       |      |       |        |
|---------------------------|--------|--------------|----------------------------|-------|------|-------|--------|
| Item                      |        | Symbol       | Conditions                 | Min.  | Тур. | Max.  | Unit   |
| Operating supply voltage  |        | Vdd          | -                          | 1.6   | 3.0  | 5.5   | V      |
| Clock supply voltage      |        | VCLK         | -                          | 1.1   | 3.0  | 5.5   | V      |
| Operating temperature     |        | Ta           | -                          | -40   | +25  | +105  | °C     |
| VDD detect voltage        |        | -VDET1       | VDD, Fall                  | 1.20  | 1.40 | 1.60  | V      |
| Frequency characteristics |        |              |                            |       |      |       |        |
| Item                      | Grade  | Symbol       | Conditions                 | Min.  | Тур. | Max.  | Unit   |
| Frequency tolerance       | А      | ∆f/f         | Ta = +25 °C<br>VDD = 3.0 V | -11.5 | -    | +11.5 | × 10⁻⁵ |
| Thequency tolerance       | В      |              |                            | -23   | -    | +23   | ~ 10   |
| Oscillation start-up time |        | <b>t</b> STA | VDD = 2.75 V<br>to 5.5 V   | -     | 0.3  | 1.0   | s      |

#### \* Refer to application manual for details

| Current consumption characteristics |   |            | Ta = -40 °C to +105 °C |      |      |
|-------------------------------------|---|------------|------------------------|------|------|
| Symbol                              | Conditions  | Ta ( °C )  | Тур.                   | Max. | Unit |
| IDD                                 | Input pins = "L",<br>FOUT = OFF, /INT = OFF,<br>VDD = VIO = 3.0 V,                              | -40 ~ +85  | 100                    | 450  | nA   |
|                                     | CHGEN = 0b, INIEN = 0b,   | -40 ~ +105 | 100                    | 1000 |      |
| · 132k                              | Input pins = "L",<br>FOUT = $32.768 \text{ kHz}$ , /INT = OFF,                                  | -40 ~ +85  | 2.0                    | 3.0  |      |
|                                     | VDD = VIO = $3.0 \text{ V}$ ,<br>FOUT pin CL = $15 \text{ pF}$ ,<br>CHGEN = $0b$ , INIEN = $1b$ | -40 ~ 105  | 2.0                    | 3.5  | μA   |

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|-------------------|---|
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